

SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT
AS AGAINST SUBSEQUENT REPROGRAMMING

ABSTRACT OF THE DISCLOSURE

For use with an integrated circuit (IC) having a testing port, a system for, and method of, securing the IC as against subsequent reprogramming and an electronic device incorporating the system or the method. In one embodiment, the system includes: (1) port inhibit circuitry located on the IC and modifiable to achieve a configuration that determines an extent to which the testing port is enabled and (2) port access circuitry, coupled to the testing port, that enables the testing port based on the configuration.